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(54) **MULTI-LAYER CONDUCTOR-DIELECTRIC OXIDE STRUCTURE**

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(58) **Field of Search** ..... 216/6, 13, 14, 216/15, 16; 29/25.01, 25.02, 25.03; 361/523, 524

(56) **References Cited**

**U.S. PATENT DOCUMENTS**

4,503,131 A *	3/1985	Baudrand	.....	428/672
5,453,294 A *	9/1995	Ogi et al.	.....	427/100
6,194,990 B1	2/2001	Lee et al.		
6,207,522 B1	3/2001	Hunt et al.		
6,342,164 B1 *	1/2002	Beuhler et al.	.....	216/39
6,541,137 B1 *	4/2003	Kingon et al.	.....	428/701
6,623,865 B1 *	9/2003	Zou et al.	.....	428/472

**FOREIGN PATENT DOCUMENTS**

WO WO01/67465 A2 9/2001

\* cited by examiner

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(57) **ABSTRACT**

A dielectric film is formed on a free-standing conductive metal layer to form a multi-layer foil comprising a conductive metal layer, a barrier layer and a dielectric oxide layer. Such multi-layer foils are mechanically flexible, and useful for the manufacture of capacitors. Examples of barrier layers include Ni—P or Ni—Cr alloys. After a second layer of conductive metal is deposited on a dielectric oxide surface opposing the first conductive metal layer, the resulting capacitor foil is processed into a capacitor. The resulting capacitor is a surface mounted capacitor or is formed as an integrated or embedded capacitor within a circuit board.

**7 Claims, 4 Drawing Sheets**

