

Chemical Trends in Impurity Incorporation into Si(100)

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(Received 10 March 1998)

Theoretical investigations of the adsorption, incorporation, and segregation of a number of common impurities at the (2×1) -Si(100) surface reveal two qualitatively distinct classes of behavior. Some impurities prefer to adsorb in trenches between surface dimer rows. Their incorporation into the surface is highly unfavorable. Other impurities prefer to adsorb on top of surface dimer rows. Their incorporation into the surface is either energetically favorable or only marginally unfavorable. The results explain a number of experimental observations. [S0031-9007(98)06884-7]

PACS numbers: 61.72.-y

The (2×1) reconstructed Si(100) surface, which is preferred for growth of Si-based devices, has complex structural and electronic properties. It is covered by rows of dimerized threefold-coordinated Si atoms, with a high density of dangling bonds and anisotropic stress fields [1–3]. Correspondingly, the adsorption, incorporation, and segregation of impurities at this surface is very complex. Acceptors like Al, Ga, and In from group III are observed to prefer the adlayer [4,5], while donors like P and As from group V prefer to incorporate into the surface [6–8]. There are exceptions to these trends. In group III, B incorporates into the surface [9,10] while, from group V, Sb remains in the adlayer [4].

A number of investigations have addressed the incorporation of As from the adlayer into the Si(100) surface [6,7]. Two factors were found to influence the observed ordering [5,11] of group III impurities in the adlayer and the segregation [12,13] of group V impurities to the surface: preference for threefold coordination and passivation of surface dangling bonds. In the case of carbon, strain effects [14,15] arising from its large atomic-size mismatch with Si determine its segregation and ordering. However, the marked distinction between the behavior of group III and group V impurities at the Si(100) surface is not readily understood on the basis of the above considerations, as all of these impurities prefer threefold coordination and have atomic sizes covering a very large range. Furthermore, the mechanism of incorporation of other donors and acceptors into this surface remains to be addressed.

In this paper, we report quantitative calculations of the energetics of adsorption and incorporation of a number of acceptor, donor, and isovalent impurities at the Si(100) surface. Al, Ga, In, and Sn exhibit qualitatively similar behavior. They prefer to adsorb in the trenches between surface dimer rows and their incorporation into the surface is highly unfavorable. Impurities in the other class (which include C, B, P, As, and Sb) prefer to adsorb on top of surface dimer rows. Their incorporation into the surface is either energetically favorable or only marginally unfavorable. These results explain a number of experimental observations and provide information

useful in atomistic modeling of the initial stages of growth and doping of submicron devices.

The total energies of the various structures were calculated using density functional theory [16], within the local density approximation. Norm-conserving pseudopotentials [17], real-space multigrid algorithms [18], and slab supercells with a (4×4) surface unit cell and 6–10 Si layers were employed. The dangling bonds in the bottom surface layer were saturated by H atoms. Energy differences were found to be converged to within 0.05 eV with a real space grid spacing of 0.6 (0.4) a.u. for group V (III) impurities, a vacuum layer thickness of 13.5 a.u., six Si layers, and one k point at Γ . Our results for energy differences between different addimer configurations for Si and Al on Si(100) are in excellent agreement with prior work [11,19]. A conservative estimate of our error bars is 0.1 eV.

At the clean Si(100) surface, the surface Si atoms order in rows of dimers and are only threefold coordinated [2]. The energy of this surface is further reduced by buckling of the dimers [1], which opens up a Peierls' gap in this initially metallic system. In each dimer one atom is raised and accepts charge from the other atom that, in turn, is lowered from its initially symmetric position. The relaxed geometry we obtained for the clean Si(100) surface showed the expected $c(4 \times 2)$ ordering of buckled Si dimers [1].

We now compare the adsorption of Al, Ga, and In, which are acceptors, to that of P, As, and Sb of group V, which are donors. All of these impurities adsorb as dimers, with each impurity having threefold coordination and saturating two surface dangling bonds. Four stable addimer configurations are known on Si(100), with positions either on top of dimer rows or in the trenches, and oriented either parallel or perpendicular to the underlying Si dimers [19]. The relative energetics of these configurations separates these impurities into two distinct classes. Group III addimers prefer to adsorb in a trench, oriented parallel to the surface Si dimers [see Fig. 1(a)]. Displacing the addimer to the top of a dimer row, while maintaining its orientation, costs about 0.3–0.5 eV. Rotating the addimer to a perpendicular orientation, whether in a trench or on top of a row, costs at least 1.0 eV. Group V addimers, on the other hand,

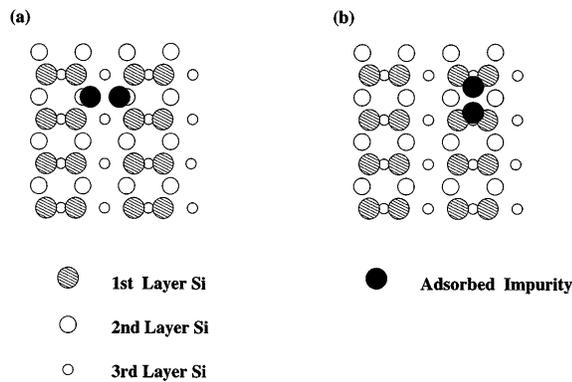


FIG. 1. The two generic equilibrium modes of adsorption of donor and acceptor dimers on Si(100).

prefer the configurations on top of the dimer rows over those in the trenches. Their equilibrium orientation is perpendicular to the surface Si dimers, as shown schematically in Fig. 1(b). Rotating the addimer to a parallel configuration costs about 0.5 eV, while both configurations in the trenches are more than 1.0 eV higher in energy.

Next, the energy of incorporation of each impurity addimer (X_2) into the near-surface region was calculated. Two qualitatively different processes were investigated, where the impurity addimer incorporates into the surface layer as (i) one unit, ejecting a surface Si dimer which moves into the adlayer, or (ii) two substitutional isolated impurities in the surface layer or in a near-surface layer, resulting in the ejection of two host Si atoms, which move into the adlayer. In terms of the supercell energies, the energy change for the process (i) is given by

$$\Delta E = E_S[(X_2)^{inc}] + E_S[(Si_2)^{ad}] - E_S[(X_2)^{ad}] - E_S \quad (1)$$

and that for process (ii) is given by

$$\Delta E = 2E_S[(X)^{inc}] + E_S[(Si_2)^{ad}] - E_S[(X_2)^{ad}] - 2E_S. \quad (2)$$

Here, E_S stands for the energy of the clean surface supercell. Impurity configurations near the surface are indicated in parentheses, and “ad” and “inc” indicate adsorbed and incorporated configurations, respectively. The Si addimer prefers to adsorb on top of a dimer row with the orientations parallel or perpendicular to the surface Si dimers being within 0.1 eV of each other [19]. With the above convention, the energy of its exchange with a surface Si dimer is zero. Process (i) has already been observed experimentally [6,8].

The calculated incorporation energies reveal striking trends. The six impurities (P, As, Sb, Al, Ga, and In) discussed thus far are all well-known surfactants [20], due to their preference for threefold coordination. Their most favorable configurations are either in the adlayer or in the top surface layer. In each case, subsurface incorporation is higher in energy by at least 1.0 eV per impurity pair. The energies of incorporation from the adlayer into the surface layer, on the other hand, fall into two distinct

classes. They are greater by 0.5–1.0 eV for group III impurities over those of group V impurities.

The results for the first mode of incorporation (i), where the impurity addimer replaces a surface Si dimer, are plotted in Fig. 2(a). In this mode, there is relatively little change in the chemical bonding configuration of the impurity dimer. For P and As, the process is favorable by about 0.2 eV per addimer, while for Sb it is unfavorable by about 0.1 eV per addimer. However, for group III impurities this process is unfavorable by 0.5–1.3 eV. In Fig. 2(b) the results for the second mode of incorporation (ii) are given. Here, two impurities replace Si atoms in two distinct Si dimers in the surface layer, giving rise to two impurity-Si heterodimers. In this mode, Al and Ga lose more than 0.5 eV per addimer, while P and As gain about 0.5 eV per addimer. Even the atoms that have big atomic size mismatches with Si preserve this trend; Sb gains only about 0.1 eV, while In loses almost 1.0 eV.

We now turn to boron. The equilibrium configuration for the B addimer is very similar to that of P, As, and Sb.

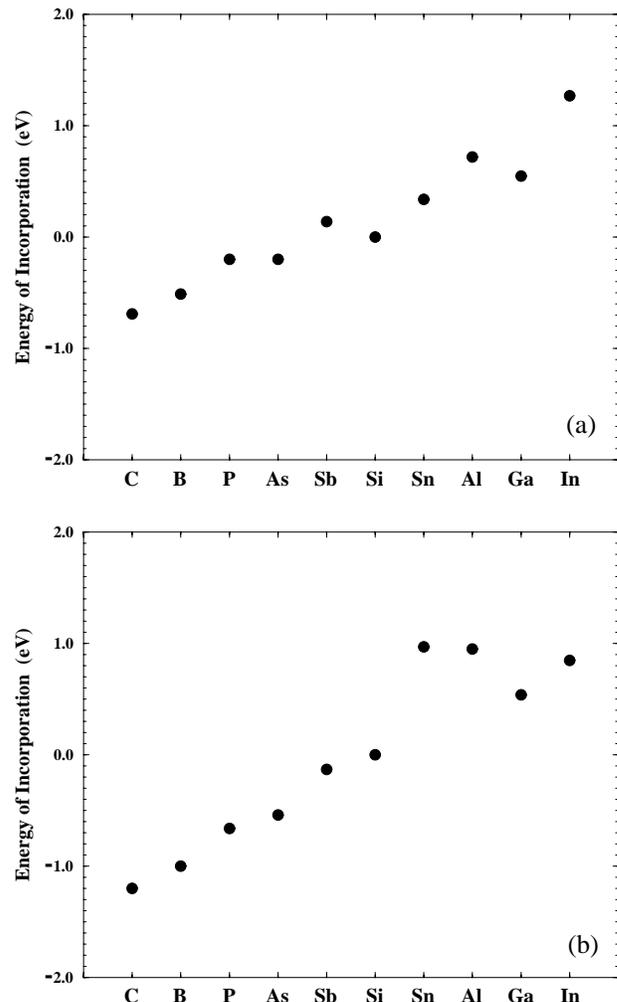


FIG. 2. (a) Energy of incorporation of each element as one surface impurity dimer on Si(100); (b) Energy of incorporation of each element as two surface impurity-Si heterodimers on Si(100).

Further, like P and As, a B addimer prefers to incorporate into the surface. The energy gain for the formation of two isolated surface B-Si heterodimers is 1.0 eV, significantly more than the 0.5 eV gained on the formation of a surface B dimer. However, boron is anomalous compared with P and As as it prefers to incorporate into the second layer as well. The energy of the isolated second-layer impurity is almost degenerate with that in the first layer. Two second-layer impurities bind by about 0.2 eV when they form a pair. This pair has two nearly degenerate orientations, parallel or perpendicular to the surface Si dimers, as shown in Fig. 3. Incorporation into the third layer or any deeper layer is unfavorable by over 1.0 eV per pair, in common with the other impurities considered.

It is thus clear that the adsorption and incorporation of the above donors and acceptors at Si(100) fall into two distinct classes. To explore the broader relevance of this classification, we investigated C and Sn, which are isoelectronic with Si. Carbon is found to behave like P, As, and B. It prefers adsorption on top of the dimer rows as opposed to the trenches, and is favored to incorporate into the surface. Two isolated Si-C heterodimers are more stable than a surface C-C dimer. However, like B, C favors both surface and subsurface incorporation. Incorporation of a C addimer into the first, second, or third layer results in a gain of 1.0 eV. On the other hand, the Sn dimer prefers to adsorb in the trenches between dimer rows, oriented like Al, Ga, and In. Furthermore, its incorporation into the surface is unfavorable by over 0.3 eV per pair and subsurface incorporation is even more unfavorable.

In all adlayer and surface configurations above, the impurities are threefold coordinated and have atomic sizes covering a wide range. Therefore, coordination or strain effects do not explain the results. Valence is not the distinguishing feature either, as B and C fall into the same class as P, As, and Sb, while Sn falls into the same class as Al, Ga, and In. Nor is there a correlation of the results with addimer bond lengths. For instance, in equilibrium,

the Sb-Sb addimer's bond length differs from that of the In-In addimer by only about 0.1 Å and yet their configurations are distinct.

The quantitative results on the interaction of the impurities with Si(100), which were obtained using fully quantum mechanical calculations, are now correlated with empirical knowledge regarding their chemical properties. If the admixture of an impurity X from the adlayer into the surface layer in isolated substitutional configurations is energetically favorable (unfavorable), it indicates that two surface Si- X bonds are stronger (weaker) than a $X-X$ bond and a Si-Si bond. In the binary phase diagrams [21] of each of the above impurities with Si, it is seen that P, As, Sb, B, and C do react exothermically with bulk Si and form a number of stoichiometric compounds, while Al, Ga, In, and Sn have extremely small miscibility with Si at any temperature. Thus the results that we obtained are consistent with bulk data that indicate significant differences in the strengths of the relevant chemical bonds.

A number of factors influence the strength of a heteropolar bond between two elements, one of which is the extent of charge transfer when the bond is formed. Generally, this is quantified using electronegativity tables. In Pauling's scale, Si is assigned an electronegativity that is higher than those corresponding to Al, Ga, and In, but lower than those of C, B, P, As, Sb, and Sn. Again, this empirical grouping exhibits rough correlation with our results. When the relative energies of different surface configurations of these impurities are examined, subtle variations related to varying charge-transfer effects are noted. A surface impurity-Si heterodimer may be flat or it may be buckled, with the top atom accepting charge from the bottom atom [1]. For P, As, and Sb, the stable heterodimer configurations are all buckled, and the impurity occupies the top position in the lowest energy configuration. The other buckled configuration is about 0.3 eV higher in energy. However, the buckled configuration with the impurity atom in the top position is unstable for Al, Ga, and In. The equilibrium configuration is nearly flat and favored by about 0.3 eV over that with the impurity in the bottom position. The small atomic size of B and C relative to Si results in only the flat configuration of the corresponding heterodimer being stable.

Our results explain a variety of experimental investigations. Indeed Al, Ga, and In of group III prefer the adlayer. In group V, P and As readily incorporate into the surface [6,8], while Sb does not incorporate to any significant extent [4]. For B, different models of subsurface B incorporation into the second [9] or the third layer [10] of the surface have been proposed in the literature. Our calculations support the former model over the latter. The results for C and Sn are also consistent with published theory and experiment [4,14,15].

One intriguing result of the calculations is that P-Si and As-Si heterodimers in the surface are more stable than P-P and As-As dimers, respectively. Generally, As incorporation has been investigated [6] at moderate

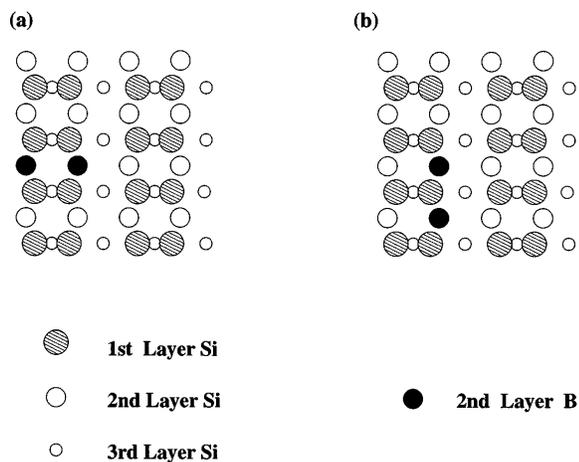


FIG. 3. The two nearly degenerate lowest energy modes of incorporation of B atoms, which form pairs in the second layer, underneath a row of surface Si dimers.

temperatures ($\sim 400\text{--}500\text{ }^\circ\text{C}$) via deposition of As_2 and As_4 molecules and only As dimer incorporation was observed. However, in a recent investigation [8] of P incorporation via decomposition of PH_3 at about $650\text{ }^\circ\text{C}$, extensive coverage of the surface by both P-Si and P-P structures was observed. It is important to understand the nature of the kinetic effects that favor incorporation of the impurities as dimers or as heterodimers.

Native point defects assist impurity incorporation, much as they assist impurity migration in bulk Si [22]. We report here the results of our systematic investigations of the formation energies of native point defects at and near the surface. The lowest energy defects were Si adatoms and surface divacancies, with formation energies of only 0.1 and 0.2 eV, respectively. Si adatoms require 0.5 eV to form, and vacancies in both the first and second layers require about 0.9 eV. Vacancies and interstitials that are deeper than the second surface layer have formation energies greater than 1.5 eV. These values are lower than those of bulk vacancies and interstitials, which are 3.3 and 3.9 eV, respectively.

The low formation energies of surface divacancies make them prevalent at temperatures as low as 300 K [23,24]. Furthermore, extensive migration of impurity addimers has been observed at 450 K [25], implying a low diffusion activation energy barrier. A migrating P or As addimer can thus enter the surface as one unit, replacing a surface divacancy. If the impurities are deposited as molecules, whether elemental or as compounds, the formation of a Si-impurity heterodimer would first require the decomposition of the appropriate molecule or an "atomic exchange" process. In the decomposition case, each impurity atom that is released would need to replace a surface vacancy or kick out a surface Si atom, forming a Si adatom in turn. The formation energies of two Si adatoms is over 1.0 eV and that of two near-surface vacancies is about 1.8 eV. Therefore, it is clear that the decomposition pathway is energetically unfavorable. Turning to an atomic exchange process, it would be competitive only if a low energy pathway that minimized the number of bonds existed for the case of the adsorbed dimers. The experimental data suggest that a low energy pathway does not exist.

Our results for the adsorption of group III impurities are in excellent agreement with earlier calculations (Refs. [5] and [11]). We are also in qualitative accord with Yu *et al.* (Ref. [7]) for the adsorption and incorporation of As, but there are significant quantitative differences as they used smaller (2×2) supercells. To the best of our knowledge, ours is the first comprehensive theoretical investigation of impurity incorporation into Si(100).

In conclusion, we have found that the adsorption and incorporation of a number of common impurities in Si fall into two distinct equivalence classes. Within each group,

atomic-size differences result in quantitative shifts of the incorporation energies. The majority of these impurities, which include donors, acceptors, and isoelectronic impurities, are surfactants, in that they prefer the adlayer or surface layer. Boron and carbon alone, which are first-row elements, prefer subsurface incorporation. Kinetic effects play an important role in determining the distribution of incorporated configurations. Our results provide a systematic explanation of a number of experimental observations.

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