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(54) **GALLIUM NITRIDE SEMICONDUCTOR STRUCTURES INCLUDING LATERAL GALLIUM NITRIDE LAYERS**

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This patent is subject to a terminal disclaimer.

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(52) **U.S. Cl.** ........................................ **257/103**; 257/615
(58) **Field of Search** ................................ 257/103, 106, 257/76, 74, 615; 438/481, FOR 244, FOR 268, FOR 308, FOR 484

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(57) **ABSTRACT**

A gallium nitride semiconductor layer is fabricated by masking an underlying gallium nitride layer with a mask that includes an array of openings therein, and growing the underlying gallium nitride layer through the array of openings and onto the mask, to thereby form an overgrown gallium nitride semiconductor layer. Although dislocation defects may propagate vertically from the underlying gallium nitride layer to the grown gallium nitride layer through the mask openings, the overgrown gallium nitride layer is relatively defect free. The overgrown gallium nitride semiconductor layer may be overgrown until the overgrown gallium nitride layer coalesces on the mask, to form a continuous overgrown monocrystalline gallium nitride semiconductor layer. The gallium nitride semiconductor layer may be grown using metalorganic vapor phase epitaxy. Microelectronic devices may be formed in the overgrown gallium nitride semiconductor layer.

**26 Claims, 2 Drawing Sheets**