



US006521911B2

(12) **United States Patent**
Parsons et al.

(10) **Patent No.:** **US 6,521,911 B2**
(45) **Date of Patent:** **Feb. 18, 2003**

(54) **HIGH DIELECTRIC CONSTANT METAL SILICATES FORMED BY CONTROLLED METAL-SURFACE REACTIONS**

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(*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 0 days.

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(21) Appl. No.: **09/908,766**

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(22) Filed: **Jul. 19, 2001**

Lee et al., "The Kinetics of the Oxide Charge Trapping and Breakdown in Ultrathin Silicon Dioxide", *J. Applied Physics* 73:9 (1993) Month unknown.

(65) **Prior Publication Data**

US 2002/0043666 A1 Apr. 18, 2002

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Related U.S. Application Data

(List continued on next page.)

(60) Provisional application No. 60/220,463, filed on Jul. 20, 2000.

(51) **Int. Cl.**⁷ **H01L 29/04**; H01L 31/036; H01L 31/0376; H01L 31/20

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(52) **U.S. Cl.** **257/52**; 438/482

(57) **ABSTRACT**

(58) **Field of Search** 438/481, 591, 438/482, 216; 257/347, 349, 52, 410, 200

A method of forming an insulation layer on a semiconductor substrate includes modifying a surface of a semiconductor substrate with a metal or a metal-containing compound and oxygen to form an insulation layer on the surface of the semiconductor substrate, wherein the insulation layer comprises the metal or metal-containing compound, oxygen, and silicon such that the dielectric constant of the insulation layer is greater relative to an insulation layer formed of silicon dioxide, and wherein the insulation layer comprises metal-oxygen-silicon bonds.

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40 Claims, 6 Drawing Sheets

